

<b>Notice of References Cited</b>	Application/Control No. 10/760,998	Applicant(s)/Patent Under Reexamination LIU, FANG-BIN	
	Examiner Cynthia Britt	Art Unit 2117	Page 1 of 1

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	V	"Testing and Diagnosis of Interconnect Faults in Cluster-Based FPGA Architectures" by Harris et al. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Publication Date: Nov 2002 Volume: 21, Issue: 11 On page(s): 1337- 1343 ISSN: 0278-0070 INSPEC Accession Number: 7462957
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.